



ASA-1003-02

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: C. OKAMOTO et al.  
Serial No.: 10/687,753  
Filed: October 20, 2003  
For: METHOD OF MOUNTING ELECTRONIC CIRCUIT CHIP  
Group: 2841  
Examiner: J. Vigushin

**REPLY**

**MS AF**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

November 27, 2006

Sir:

In response to the Office Action dated June 27, 2006, the period for reply thereto being extended two months by the attached Petition for Extension of Time to expire November 27, 2006, reconsideration and withdrawal of the outstanding rejections and allowance of the present application are respectfully requested in light of the following amendments and remarks.

**Amendments to the Claims** begin on page 2.

**Remarks** are included following the amendments